

F1G.2

VOLE ADATA TRANSMITTED IN ONE ISOCIINONOCS CIOLE	n-1 CIP SPH TSP SPH TSP	n CIP SPH TSP	n+1 CIP	n+2 CIP SPH TSP	n+3 CIP SPH TSP SPH TSP	n+4 CIP SPH TSP	n+5 CIP	n+6 CIP SPH TSP TSP TSP	n+7 CIP SPH TSP	n+8 CIP SPH TSP	n+9 CIP SPH TSP	n+10 CIP SPH TSP	n+11 CIP SPH TSP TSP TSP	n+12 CIP	n+13 CIP	•	•
CYCLE	n-1	c	n+1	n+2	n+3	n+4	n+5	9+u	2+u	8+u	6+u	n+10	n+11	n+12	n+13	•	•

F1G.3

:CIP HEADER(8 BYTES)

:SPH(4 BYTES)

:TSP(188 BYTES)

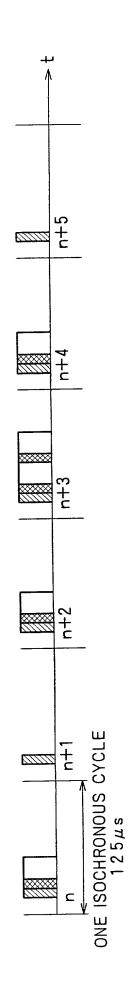
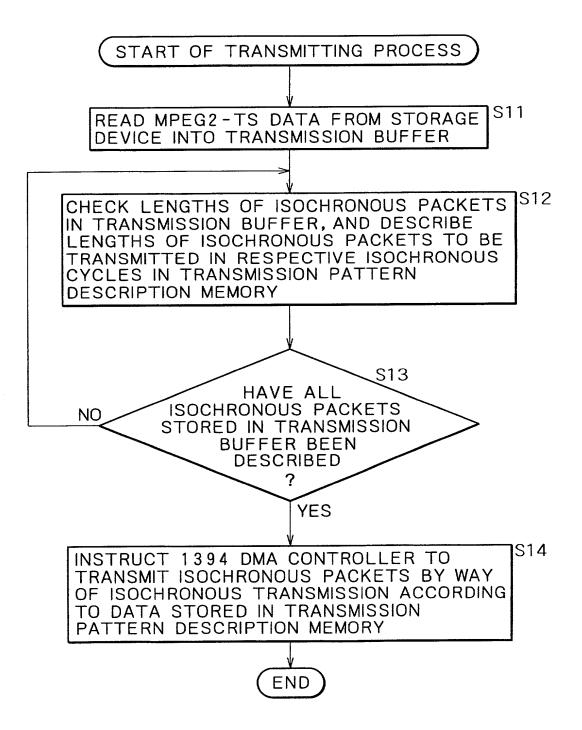
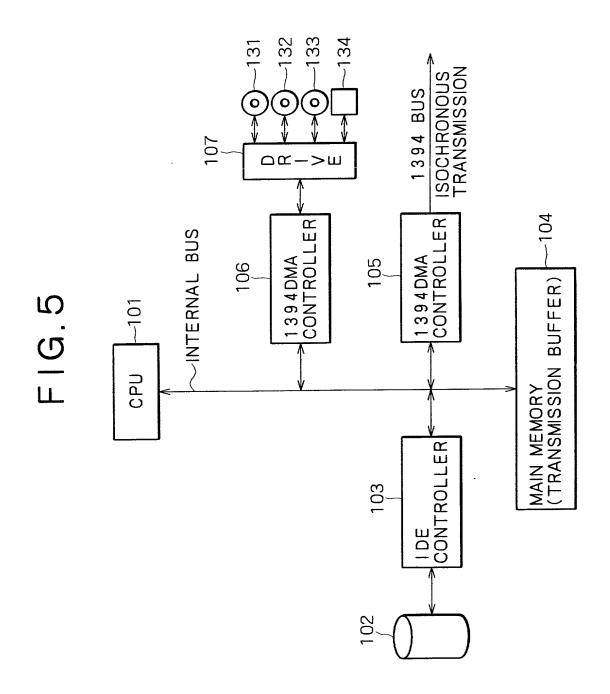


FIG.4





F1G.6

CYCLE TIME

SYCLE TIME	70	ATA T	DATA TRANSMITTED IN ONE ISOCHRONOUS CYCLE	ISOC	HRONOUS CYCLE	
n-1	CIP	SPH	TSP	SPH	TSP	
٦	CIP	SPH	TSP	SPH	NullPacket	
n+1	CIP	SPH	NullPacket	SPH	NullPacket	
n+2	CIP	SPH	TSP	SPH	NullPacket	
n+3	CIP	SPH	TSP	SPH	TSP	
n+4	CIP	SPH	TSP	SPH	NullPacket	
n+5	CIP	SPH	Nullpacket	SPH	Nullpacket	
9+u	dIO	HdS	TSP	HdS	TSP	
N+7	CIP	SPH	TSP	SPH	NullPacket	
n+8	dIO	SPH	TSP	SPH	NullPacket	
0+u	CIP	HdS	TSP	HdS	NullPacket	
n+10	dIO	HdS	TSP	HdS	NullPacket	
n+11	dIO	HdS	TSP	SPH	TSP	
n+12	CIP	SPH	NullPacket	SPH	NullPacket	
n+13	dlo	HdS	NullPacket	HdS	NullPacket	

FIG.7

NullPacket() {	No.of bits	Value
sync_byte	8	0×47
transport_error_indicator	1	0
payload_unit_start_indicator	1	0
tansport_priority	1	0
PID	13	0x1fff
Transport_scrambling_control	2	0
adaptation_field_control	2	0
countinuity_counter	4	0
for(i=0;i<184;1++){		
data_byte	8	0xff
{		

F 1 G. 8

:CIP HEADER(8 BYTES)

SPH(4 BYTES)

| :TSP(188 BYTES)

:NullPacket(188 BYTES)

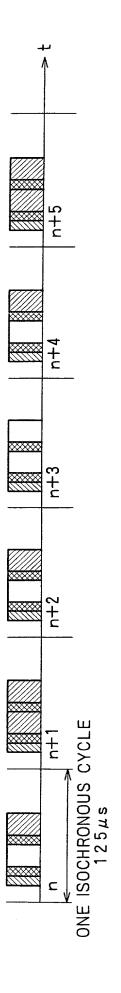


FIG.9

S101

START OF TRANSMITTING PROCESS

READ MPEG2-TS DATA FROM STORAGE DEVICE, ADD NULLPACKETS TO ISOCHRONOUS PACKETS TO EQUALIZE AMOUNT OF DATA TRANSMITTED IN ISOCHRONOUS CYCLES TO MAXIMUM TRANSMITTABLE PACKET SIZE, AND STORE ISOCHRONOUS PACKETS IN TRANSMISSION BUFEER

INSTRUCT 1394 DMA CONTROLLER TO SEND CONSTANT NUMBER OF BYTES OF DATA BY WAY OF ISOCHRONOUS TRANSMISSION FROM TRANSMISSION BUFFER IN EACH ISOCHRONOUS CYCLE

END